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**KARYASHALA**  
WORKSHOP 2023

# HIGH-END WORKSHOP ON AI/ML ALGORITHMS AND APPLICATIONS IN VLSI DESIGN AND TECHNOLOGY

(Under the KARYASHALA Scheme – A SERB initiative)

**17<sup>th</sup> - 22<sup>nd</sup> July 2023**

Website: <https://karyashala-workshop3.daiict.ac.in/>



**Funded by:** Science and Engineering Research Board (SERB), DST, Govt. of India, under the Accelerate Vigyan Scheme  
**Organized by:** Dhirubhai Ambani Institute of Information and Communication Technology, Gandhinagar, Gujarat, India

## About DA-IICT Gandhinagar

DA-IICT is a university devoted to the field of Information and Communication Technology (ICT). The institute is spread over 50 acres of land in Gandhinagar, capital city of Gujarat and has lush green campus. Established in 2001 by the Reliance Group, DA-IICT is consistently ranked as one of the top technological institutes in the country. DA-IICT has been accredited with 'A' grade by NAAC and 'Centre of Excellence' by Government of Gujarat.

The institute has established several research collaborations with different universities, government organizations, research labs, industries both in India and abroad. The students along with the faculties publish their research articles in several national/international Book Chapters, Journals and Conferences of repute. The institute has state-of-the-art research labs and super-computing facilities. The students organize and participate in several technical, cultural and sports events throughout the year. The institute possesses vast well established alumni group.

For more details, please visit <https://www.daiict.ac.in/>

## Tentative Resource Persons

The course faculty includes recourse persons from IITs, NITs, IIITs, reputed institutes and industries.

**How to Apply:** <https://forms.gle/PRifLMbvjpHBF4SE8>

## Important Dates

Registration Opens	:	13 <sup>th</sup> June 2023
Last date for Application	:	5 <sup>th</sup> July 2023
Display of shortlisted candidates	:	7 <sup>th</sup> July 2023
Workshop Dates	:	17 <sup>th</sup> - 22 <sup>nd</sup> July 2023

## About the Workshop

'KARYASHALA' is an effort to improve research productivity of promising PG and PhD students from universities and colleges through high-end workshops on specific themes. This program aims to provide opportunities to acquire specialized research skills. Following are the objectives of the current high-end workshop:

- The basics of AI/ML approaches and their implementation.
- Introduction to VLSI design and methodologies
- VLSI architectures and hardware for AI/ML algorithms
- Development of automated systems using AI/ML approaches and VLSI architectural designs
- Hands-on training using CAD tools such as Verilog, FPGA, SPICE for application oriented VLSI system design optimization and output performance parameter prediction using AI/ML
- AI/ML algorithms and FPGA implementation

## Who can attend

The workshop is open to postgraduate students/research scholars from all Institutes and Universities. Preference will be given to the students having interest towards Electronics, VLSI, Embedded Systems, Artificial Intelligence and Machine Learning Applications, HDL – Verilog, FPGA implementation.

Total number of seats is limited to 25. The participants will be selected based on their academic/research credentials and first-come-first-serve basis. The selected applicants will be provided food & accommodation at DA-IICT. Also, the traveling expenses (3AC) will be reimbursed. **There is no registration fees.**

## Coordinators:

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**Venue:** Dhirubhai Ambani Institute of Information and Communication Technology (DA-IICT) Gandhinagar, Gujarat, India