High End Workshop on Hardware Assisted Security for Fog Computing based IoT Networks

(Under the KARYASHALA Scheme – A SERB initiative)

11th – 17th July 2022

Website: https://karyashala-workshop.daiict.ac.in

Funded by: Science and Engineering Research Board (SERB), Department of Science and Technology, Government of India, under the Accelerate Vigyan Scheme

Organized by: Dhirubhai Ambani Institute of Information and Communication Technology, Gandhinagar, Gujarat, India

About DA-IICT Gandhinagar

DA-IICT is a university devoted to the field of Information and Communication Technology (ICT). The institute is spread over 50 acres of land in Gandhinagar, capital city of Gujarat and has lush green campus. Established in 2001 by the Reliance Group, DA-IICT is consistently ranked as one of the top technological institutes in the country. DA-IICT has been accredited with 'A' grade by NAAC and 'Centre of Excellence' by Government of Gujarat. The institute has established several research collaborations with different universities, government organizations, research labs, industries both in India and abroad. The students along with the faculties publish their research articles in several national/international Book Chapters, Journals and Conferences of repute. The institute has state-of-the-art research labs and super-computing facilities. The students organize and participate in several technical, cultural and sports events throughout the year. The institute possesses vast well-established alumni group. For more details, please visit https://www.daiict.ac.in/

How to Apply: https://forms.gle/tzDJmSifFMoksdch8

Tentative Resource Persons

Dr. Susmita Sur kolay, ISI Kolkata
Dr. Somitra Kumar Sanadhya, IIT Jodhpur
Dr. Rajeevan Chandel, NIT Hamirpur
Dr. Alka Agrawal, B.B.A.U. Lucknow
Dr. Sreeja Rajendran, DA-IICT Gandhinagar
Dr. Praveen Kumar Vadnala, Riscure, Netherlands
Dr. Vinay Palaparthy, DA-IICT Gandhinagar
Dr. Rajit Karmakar, Synopsys, India
Dr. Yash Agrawal, DA-IICT Gandhinagar

About the Workshop

'KARYASHALA' is an effort to improve research productivity of promising PG and PhD students from universities and colleges through high-end workshops on specific themes. This program aims to provide opportunities to acquire specialized research skills. Following are the objectives of the current high end workshop:

(a) Provide an understanding of the fundamentals of Hardware Security.
(b) Impart knowledge about basics of IoT and Fog Computing along with security issues in the IoT paradigm.
(c) Enable participants to gain insight into various aspects of Hardware Security like Hardware Trojan Detection, Prevention and Logic Locking.
(d) Familiarize participants with concepts of Hardware Security Modules and Physically Unclonable Functions in IoT applications
(e) Provide hands on training for circuit simulation and implementation on FPGA.

Who can attend

Karyashala is open to postgraduate students/research scholars from all Institutes and Universities. Preference will be given to the students having interest towards VLSI, Embedded Systems, Computer Architecture, Hardware Security, Cloud/Fog Computing, HDL – Verilog, FPGA implementation. Total number of seats is limited to 25.

The travelling allowances, fooding and accommodation will be borne by the workshop as per the institute norms. There is no registration fees. The applicants have to submit the NOC (Form available on website) from the supervisor/HoD/Head of the institute.

Important Dates:

Registration Opens	:	13th June 2022
Last date for Application	:	05th July 2022
Display of shortlisted candidates	:	06th July 2022
Workshop Dates	:	11th July 2022

Event Organizer
Dr. Yash Agrawal
yash_agrawal@daiict.ac.in
+91-98821 14669

Event Co-Organizer
Dr. Sreeja Rajendran
sreeja_rajendran@daiict.ac.in
+91-90747 03191

Venue:
Dhirubhai Ambani Institute of Information and Communication Technology (DA-IICT) Gandhinagar, Gujarat, India